

JP3: wired to CP Out of the preceding board

JP7: gated/triggrd mode
upper position= gated mode
lower position = triggered mode
(= factory setting)

gated mode = AR envelope
triggered mode = AD envelope

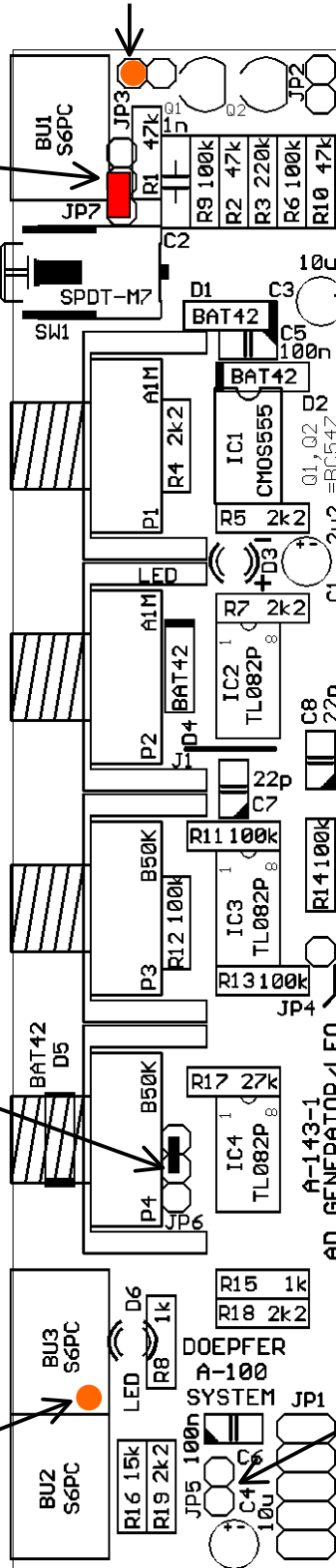
JP2: wired to socket EOA

JP6 (wire jumper):
pre/post polarizer single output
upper position: pre polarizer
(=factory setting)
lower position: post polarizer

JP4: mix bus
JP4 of all four boards
are connected

JP5: mix out
JP5 of the upper board is
wired to the mix out socket

this pin of the Cp Out socket is
wired to JP3 of the next board



grey = just for information (do not change these connections)

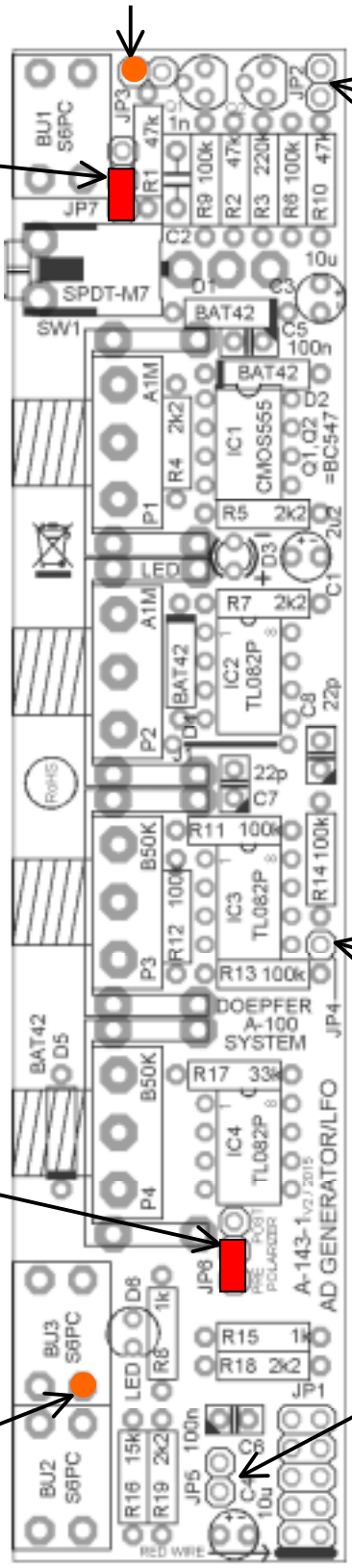
Version 1

JP3: wired to CP Out of the preceding board

JP7: gated/triggerd mode
upper position= gated mode
lower position = triggered mode
(= factory setting)

gated mode = AR envelope
triggered mode = AD envelope

JP2: wired to socket EOA



JP6: pre/post polarizer single output
upper position: post polarizer
lower position: pre polarizer
(=factory setting)

JP4: mix bus
JP4 of all four boards
are connected

JP5: mix out
JP5 of the upper board is
wired to the mix out socket

this pin of the Cp Out socket is
wired to JP3 of the next board

grey = just for information (do not change these connections)

Version 2